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Terms	Documents
L10 and data adj line	4

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result set

*DB=USPT; PLUR=YES; OP=ADJ*

<u>L11</u>	L10 and data adj line	4	<u>L11</u>
<u>L10</u>	L7 and gate and line	10	<u>L10</u>
<u>L9</u>	L7 and gate	10	<u>L9</u>
<u>L8</u>	L7 and gate adj driver	0	<u>L8</u>
<u>L7</u>	L6 and data and bus	18	<u>L7</u>
<u>L6</u>	L4 and control\$3	18	<u>L6</u>
<u>L5</u>	L4 and operation adj circuit	0	<u>L5</u>
<u>L4</u>	L3 and memories	18	<u>L4</u>
<u>L3</u>	L2 and address adj counter	18	<u>L3</u>
<u>L2</u>	L1 and address adj bus	89	<u>L2</u>
<u>L1</u>	((345/87  345/88  345/89  345/90  345/91  345/92  345/93  345/94  345/95  345/96  345/97  345/98  345/99  345/100 )!.CCLS. )	3137	<u>L1</u>